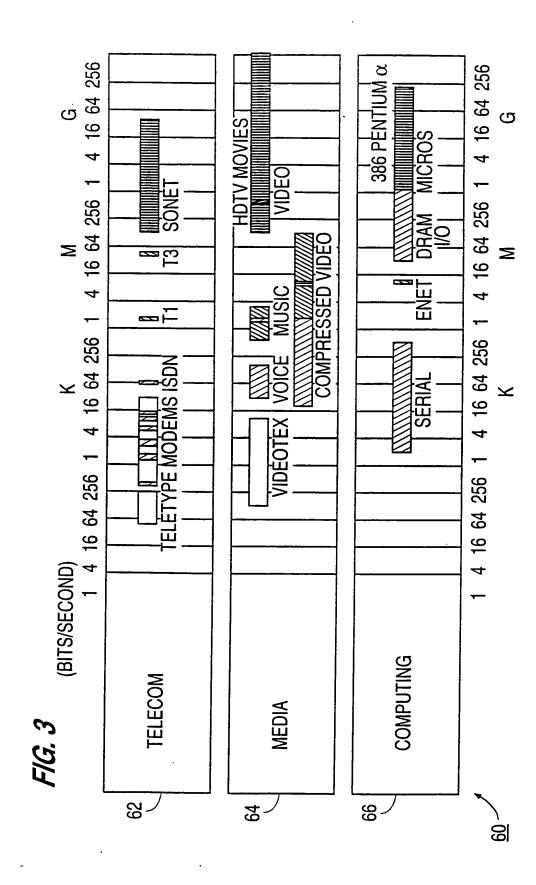
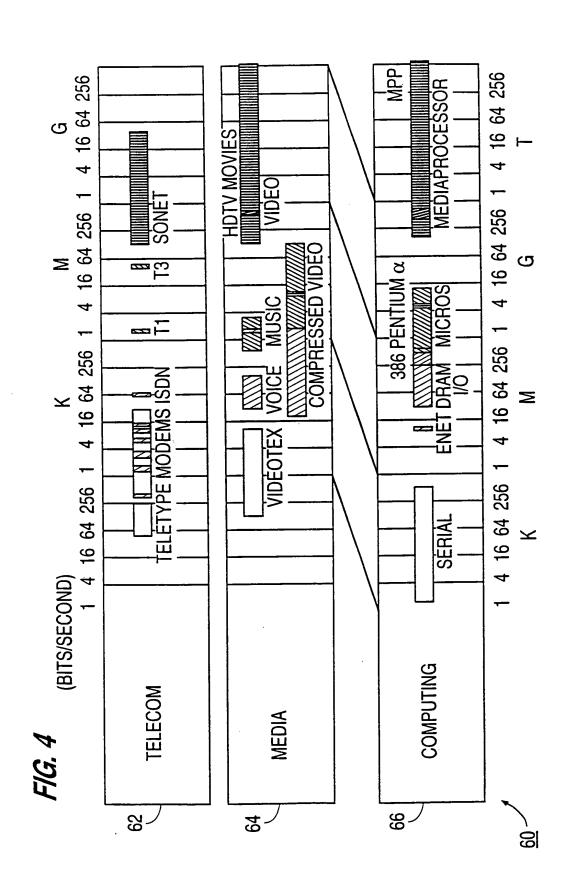


APPROVED O.G. FIG.
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APPROVED	O.G. FIG.								
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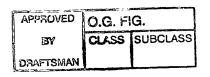
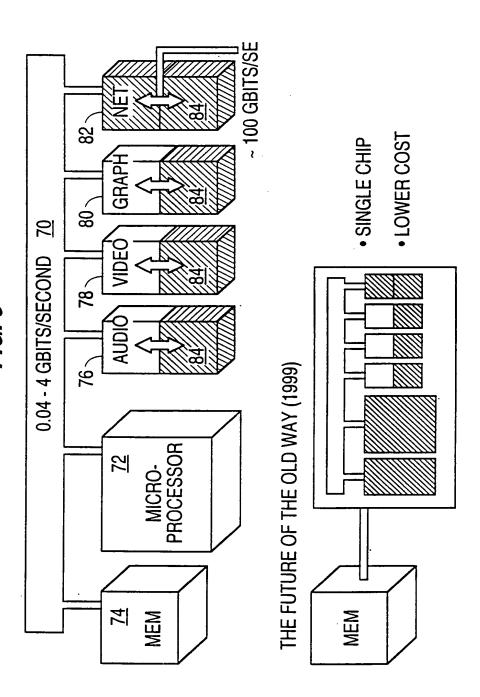
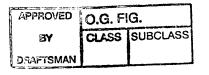


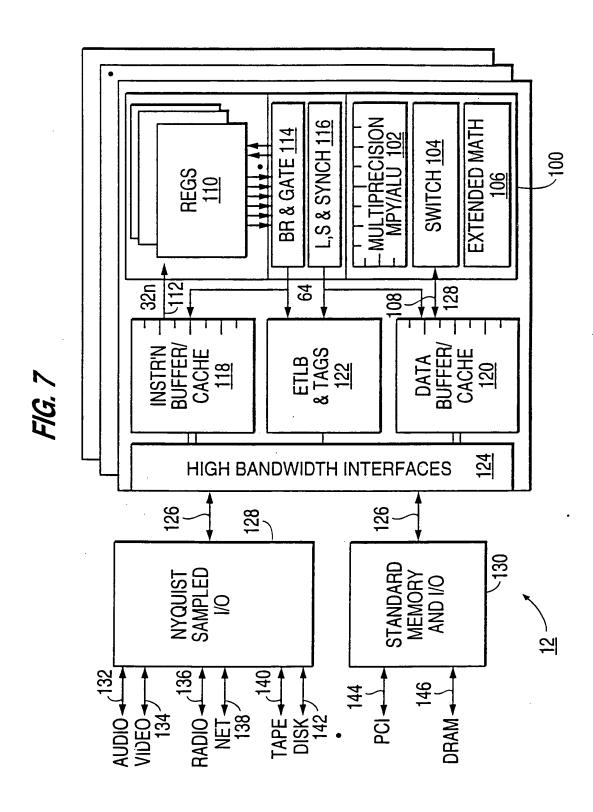
FIG. 5

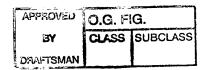


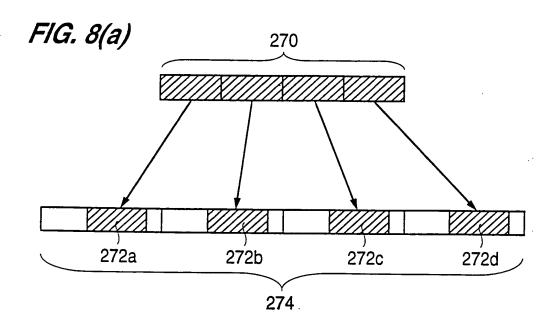
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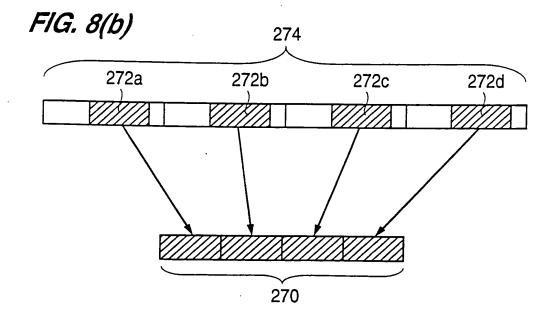
8 9 94 • MUCH LOWER COST 32 **8** ≥ 8 SMALLER CHIP 2 8 8 MEM MEDIAPROCESSOR 400 GBIT/SECOND FIG. 6 8 PROC THE FUTURE OF THE UNIFIED WAY (1995) <u>Q</u> 8 MEM 8 MEM 엉 MEM 8 8 MEM MEM











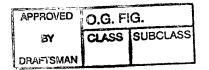


FIG. 8(c)

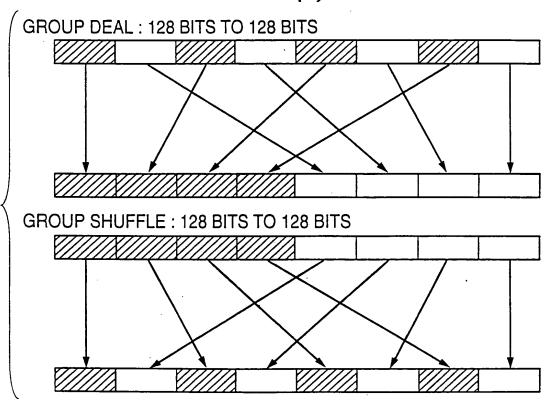
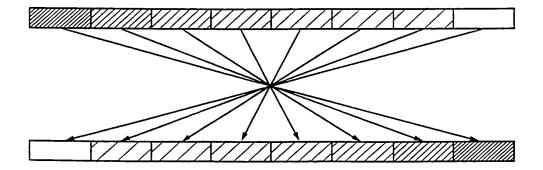
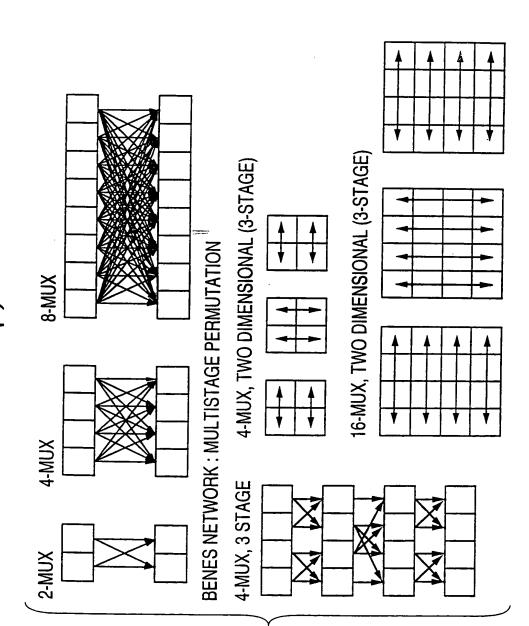


FIG. 8(d)



APPROVED	O.G. FIG.								
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FIG. 8(e)



O.G. FIG.

Y CLASS SUBCLASS

FIG. 9(a)

1	31		24	23			•							•	0	
		major				•	. –		im	m						
	280	8							2	4						
	31		24	23		18	17								0	
		major			ra						imm					•
	280	8			6	·					18					
	31		24	23		18	17	`	12	11					0	
{		major			ra			rb				im	m			
	280	8			6			6				1	2			
	31		24	23		18	17		12	11		6	5		0	
		major			ra			rb			rc			minor		
		8			6			6			6			6	$\overline{}$	282
	31		24	23		18	17		12	11		6	5		0	
		major			ra			rb			rc			rd		
	280	8			6			6			6 .			6		

APPHOVED	O.G. FIG.							
BY	CLASS	SUBCLASS						
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significand 112 significand 25 significand sign exponent significand 23 double single half 14 10 9 112 111 quad 5 52 51 23 22 15 exponent exponent exponent 9 5 sign 31 62 sign 63 126 sign 127

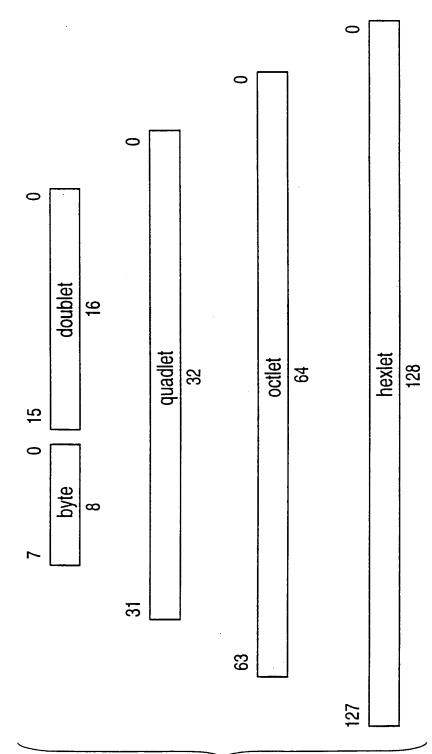
FIG. 9(b)

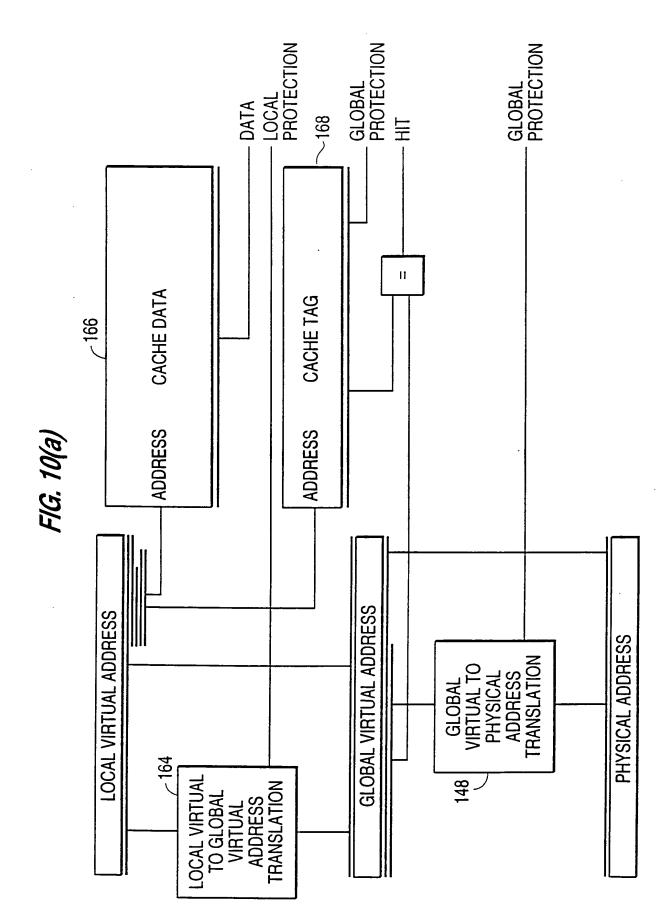
O.G. FIG.

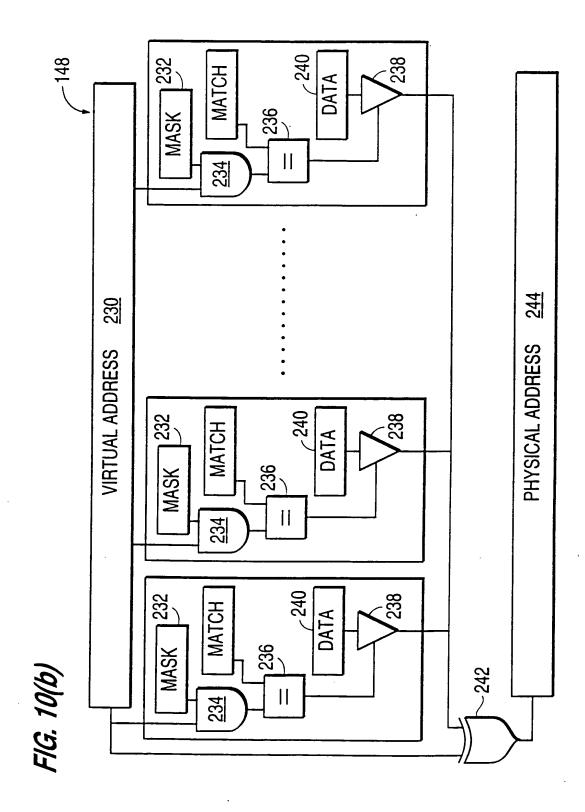
BY CLASS SUBCLASS

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FIG. 9(c)





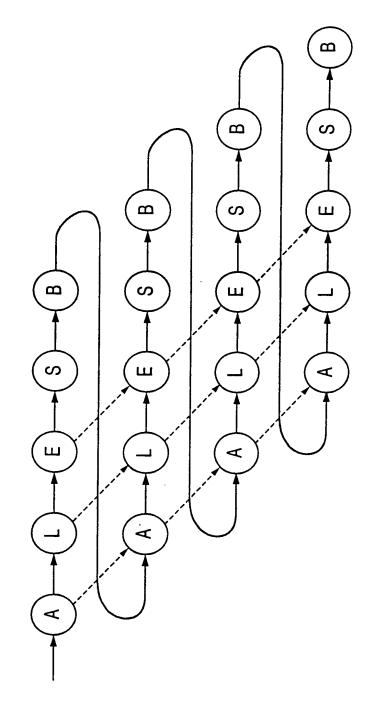


O.G. FIG.

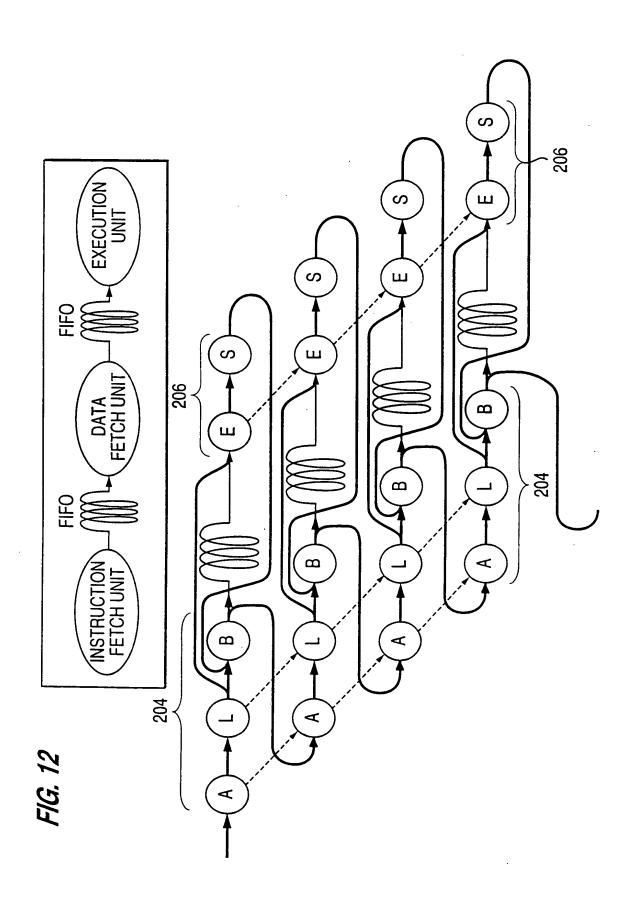
O.G. FIG.

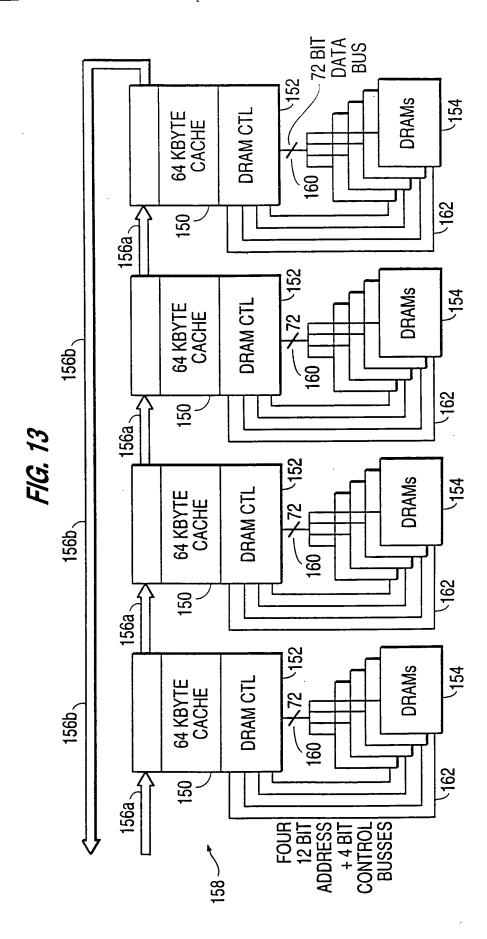
SUBCLASS

FIG. 11



APPROVED O.G. FIG.
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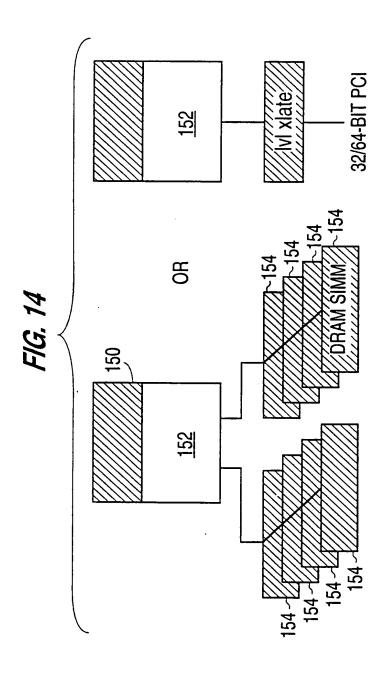




O.G. FIG.

CLASS SUBCLASS

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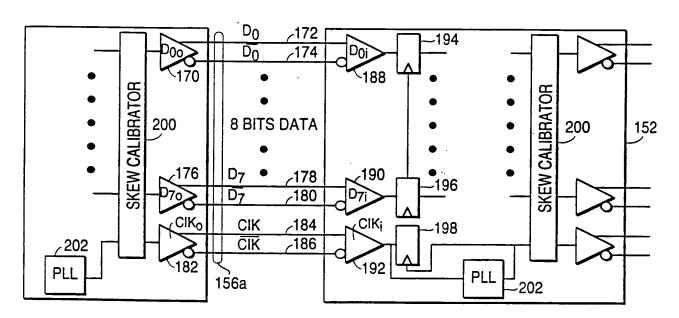


O.G. FIG.

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FIG. 15



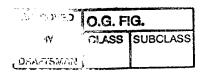
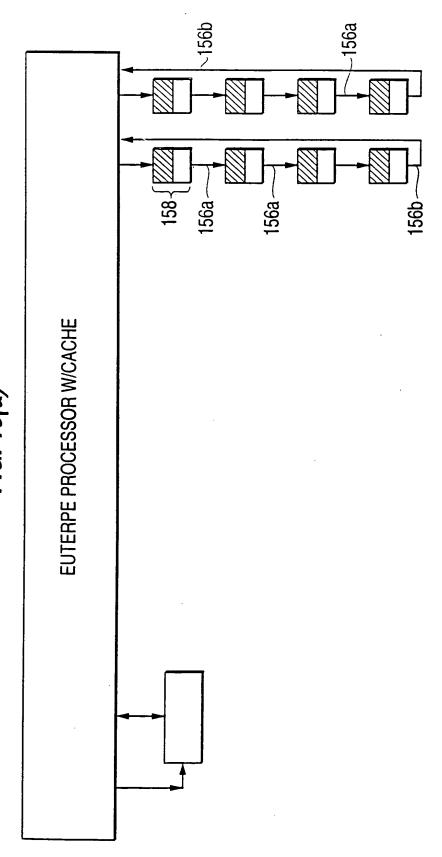
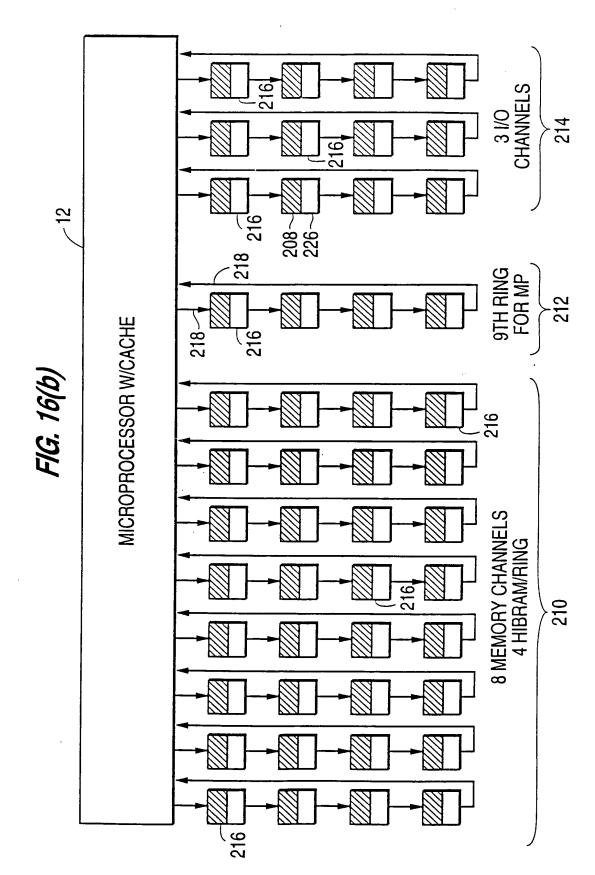
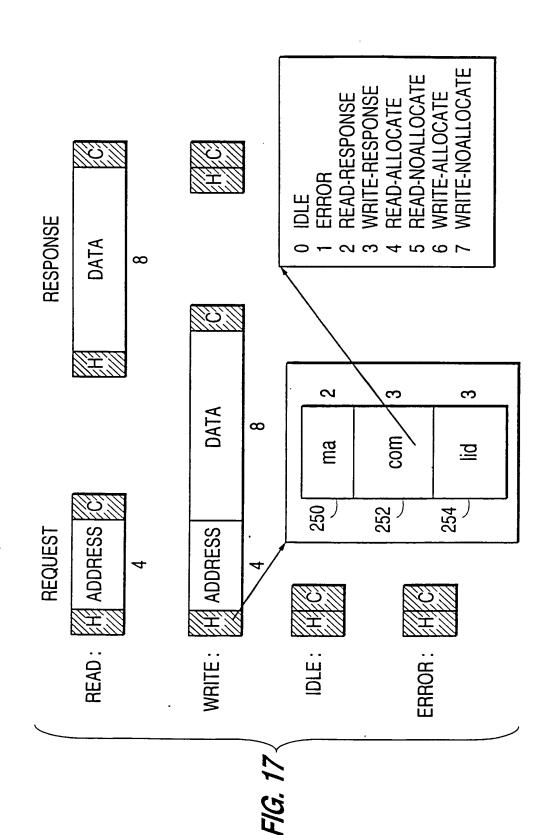
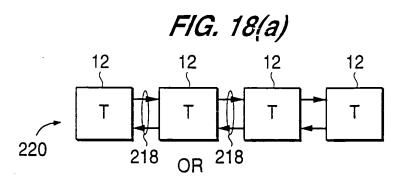


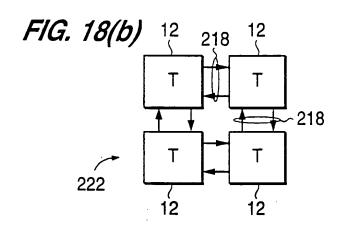
FIG. 16(a)

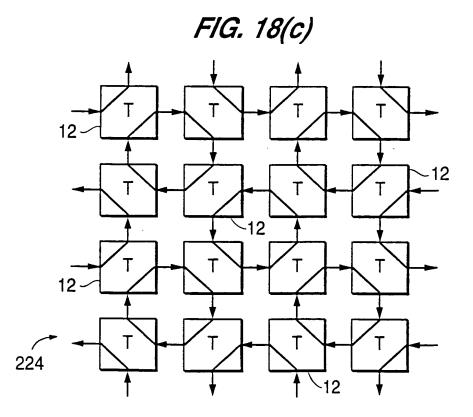












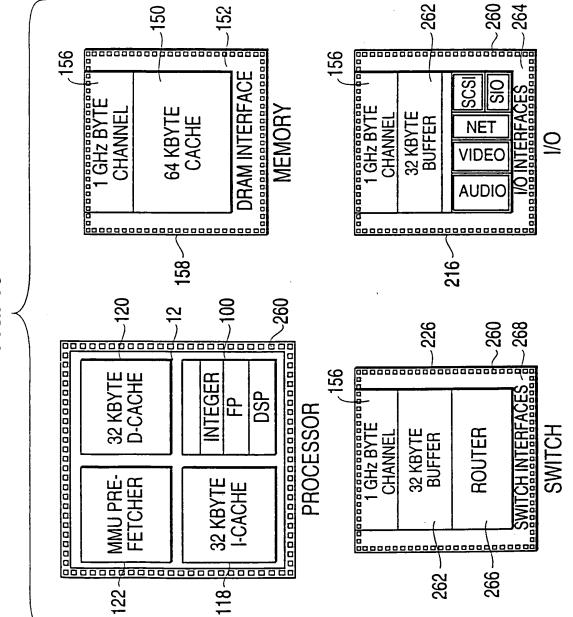


FIG. 19

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